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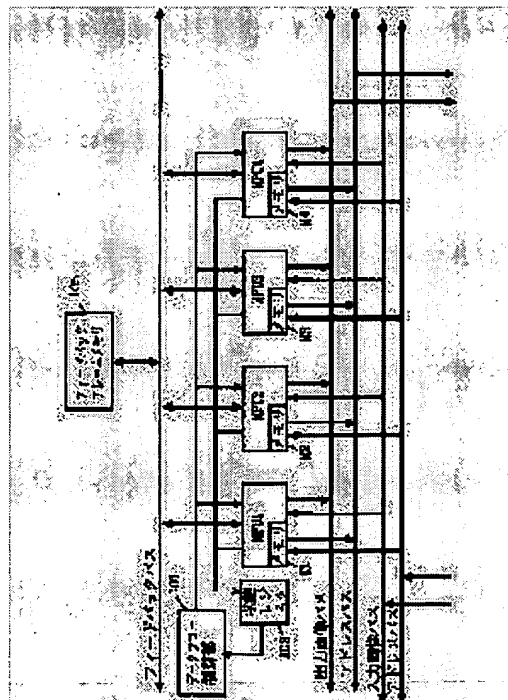
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(54) IMAGE PROCESSOR

(57)Abstract:

PROBLEM TO BE SOLVED: To efficiently process image data by a circuit which divides one piece of image data into pieces and processes them by MPUs in parallel.

SOLUTION: The image data inputted through an input image bus is processed by MPUs 1 to 4 in parallel. The address of the image data is inputted from an address bus and address memories M1 to M4 provided to the respective MPUs 1 to 4 store the addresses of the image data processed by the respective MPUs. After the image data are processed, the image data are outputted through an output image bus and the addresses of the image data are outputted through the address bus.



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ABSTRACT:

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